IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1 – 30. (Cancelled)

31. (new)   A method for data protection comprising:

intercepting change information representing a change made by a process running on a computer system to a file on the computer system; and

transmitting the change information through a network, wherein the transmission is initiated by the computer system substantially concurrently with a time the change to the file occurs.

32. (new)   The method of claim 31 further comprising receiving a selection of the file as one to be mirrored.

33. (new)   The method of claim 31 wherein the file is one of a plurality of files to be mirrored and intercepting the change information comprises:

intercepting all change information representing a change to any file on the computer system; and

for each set of change information that is intercepted, determining whether the change information is associated with one of said plurality of files to be mirrored.

34. (new)   The method of claim 33 wherein the plurality of files to be mirrored comprises a subset of the files stored on the computer system.

35. (new) The method of claim 31 wherein the change is a write operation.

36. (new) The method of claim 31 wherein the change is a file operation.

37. (new) The method of claim 31 wherein the file is accessed by an application program.

38. (new) The method of claim 31 wherein the change information is transmitted to a second computer.

39. (new) The method of claim 31 wherein the change information is transmitted to a remote computer.

40. (new) A system for data protection comprising:

a processor configured to intercept change information representing a change made by a process running on a computer system to a file on the computer system and transmit the change information through a network, wherein the transmission initiated by the computer system is performed substantially concurrently with a time the change to the file occurs; and

a memory coupled with the processor, wherein the memory is configured to provide the processor with instructions.